

100

ADAPTIVE COMPUTING ENGINE (ACE)

FIG. 1

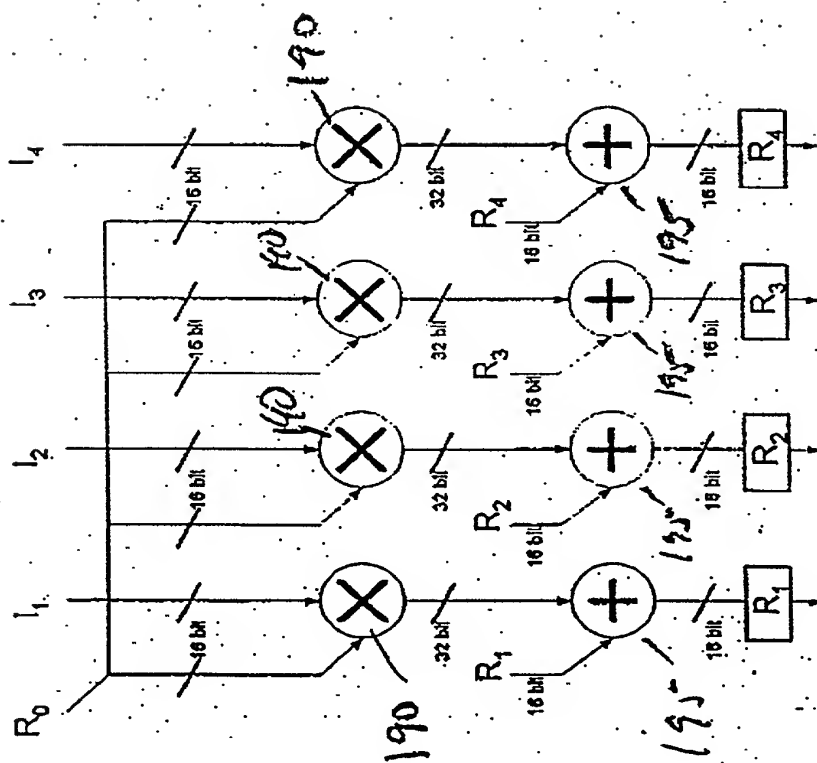


FIG. 2

TO OTHER MATRICES 150  
(INCLUDING CONTROLLER 120 AND  
MEMORY 140)

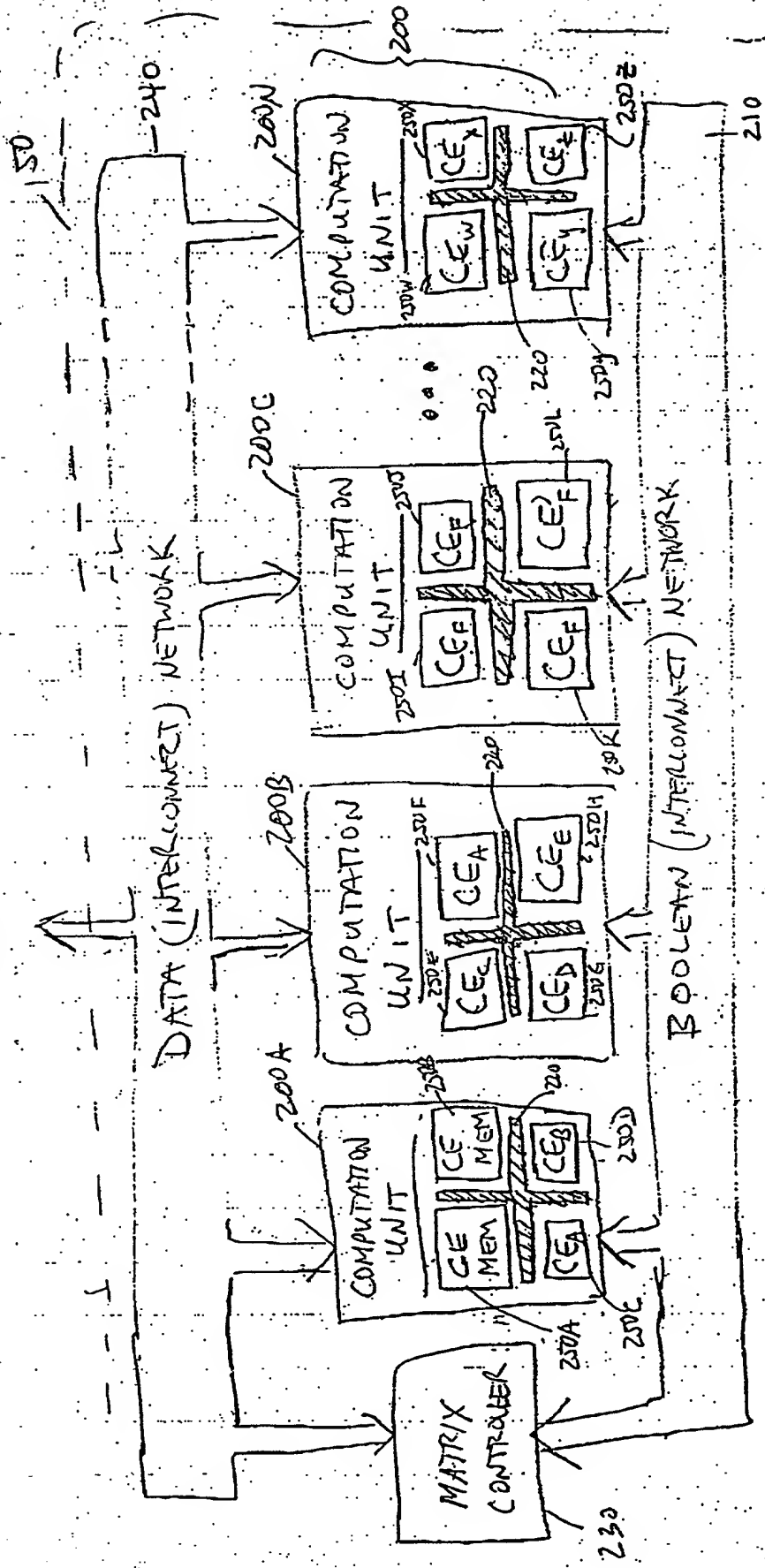


FIG. 3

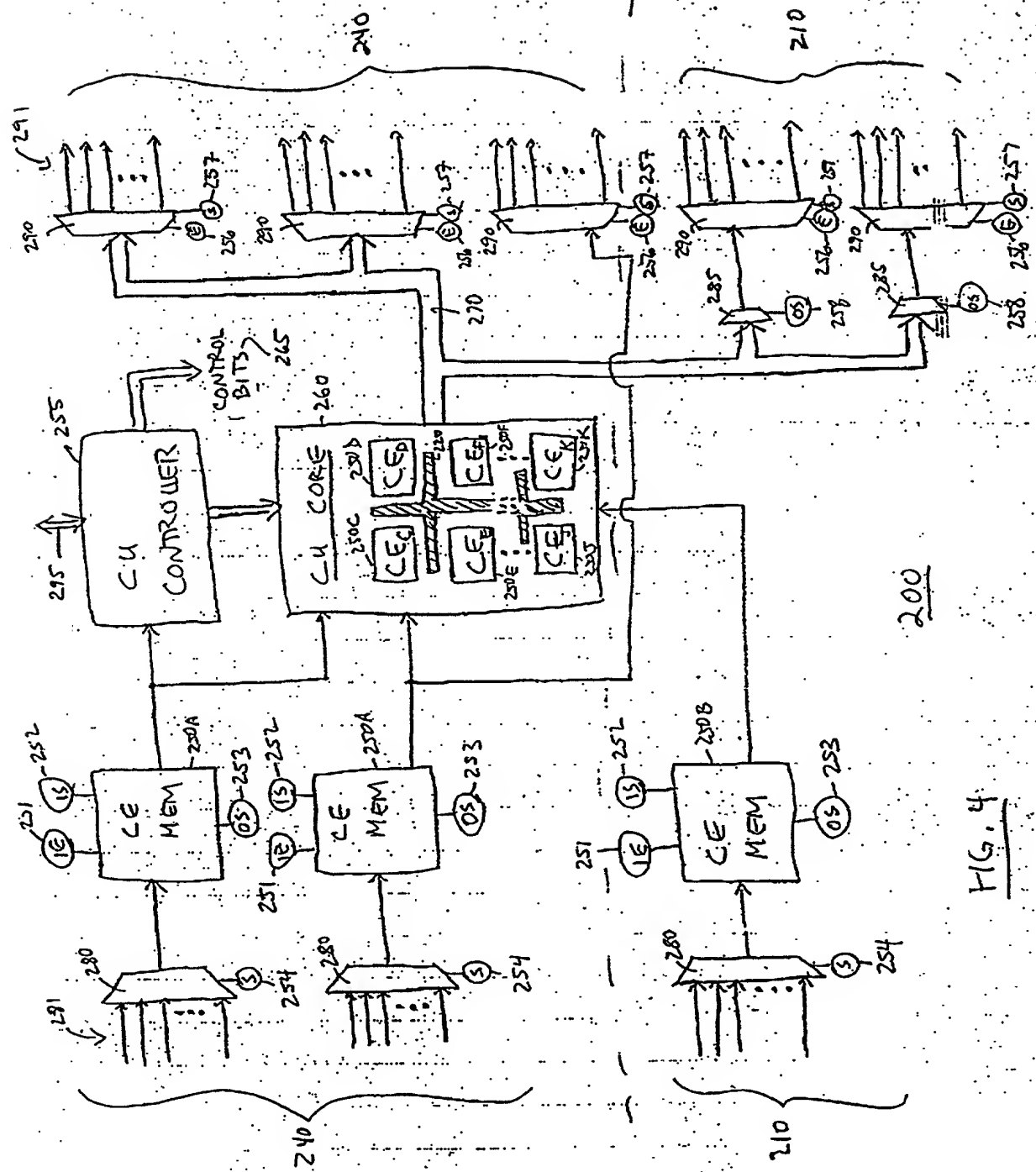


FIG. 4

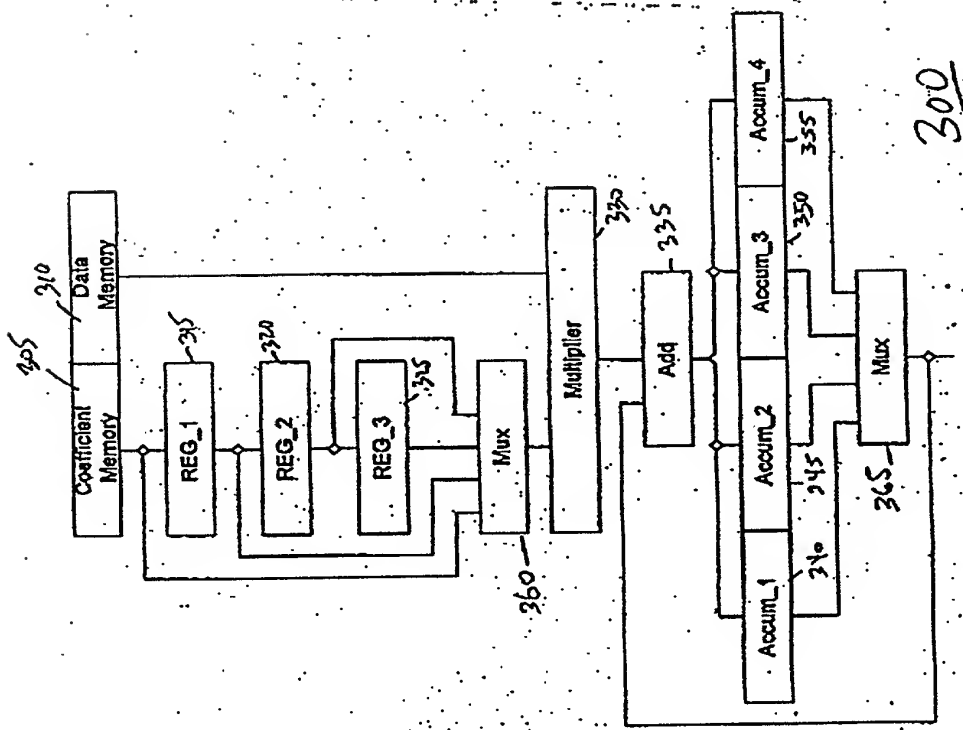
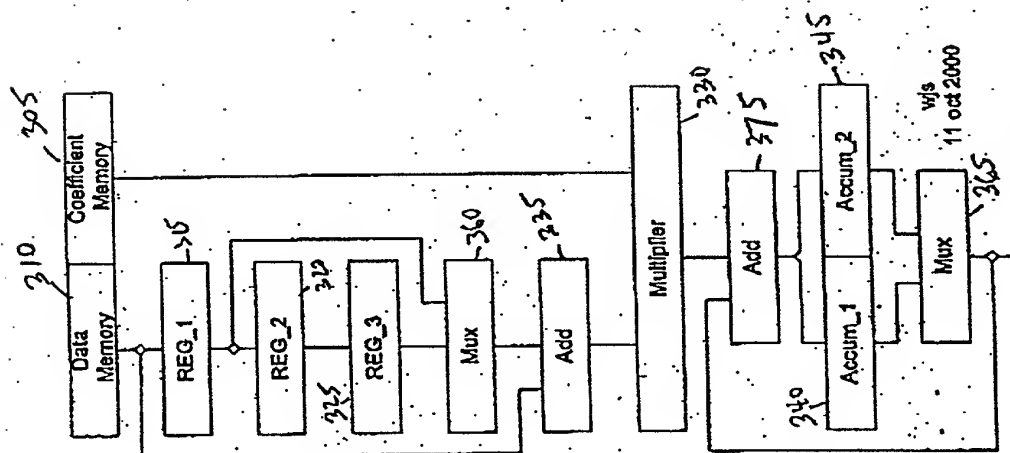


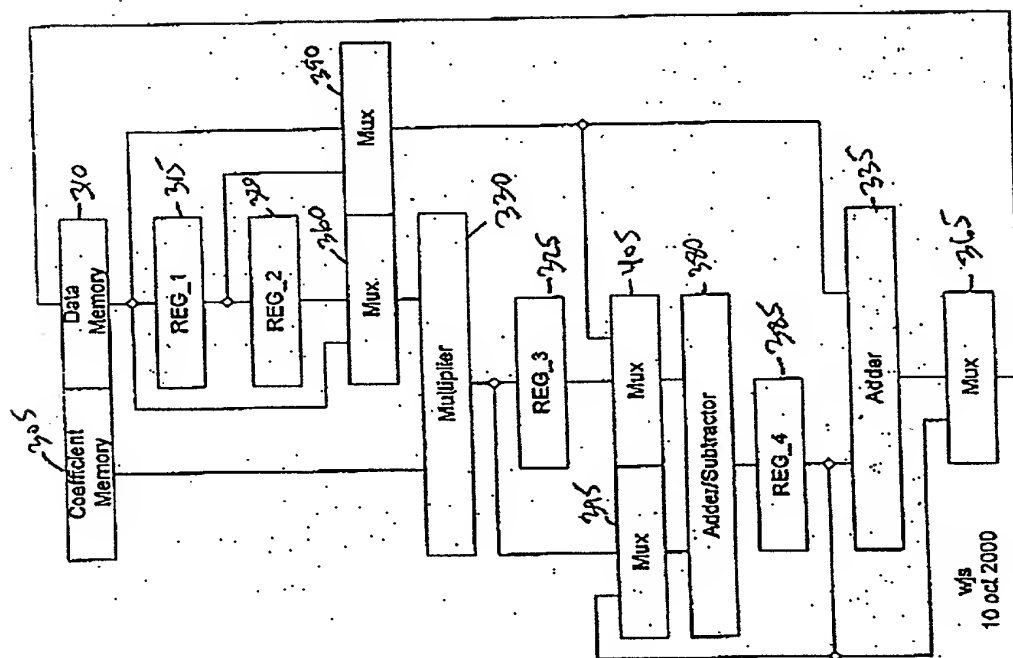
FIG. 5A

300



370

FIG. 5B



400

FIG. 5C

w/s  
10 oct 2000

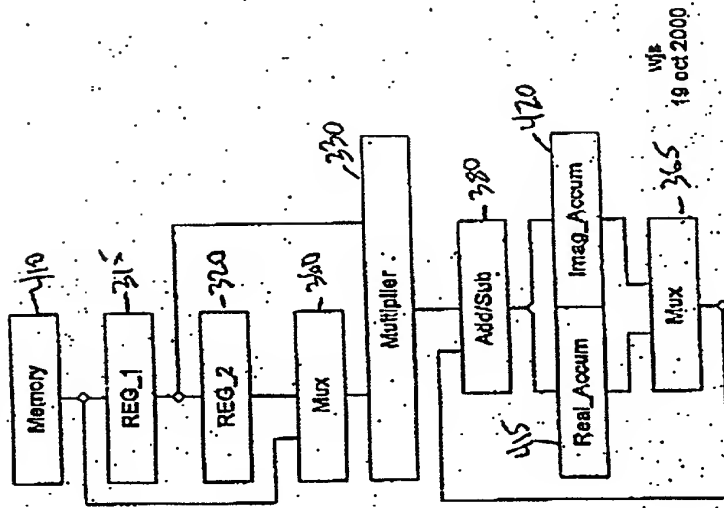


FIG. 5D



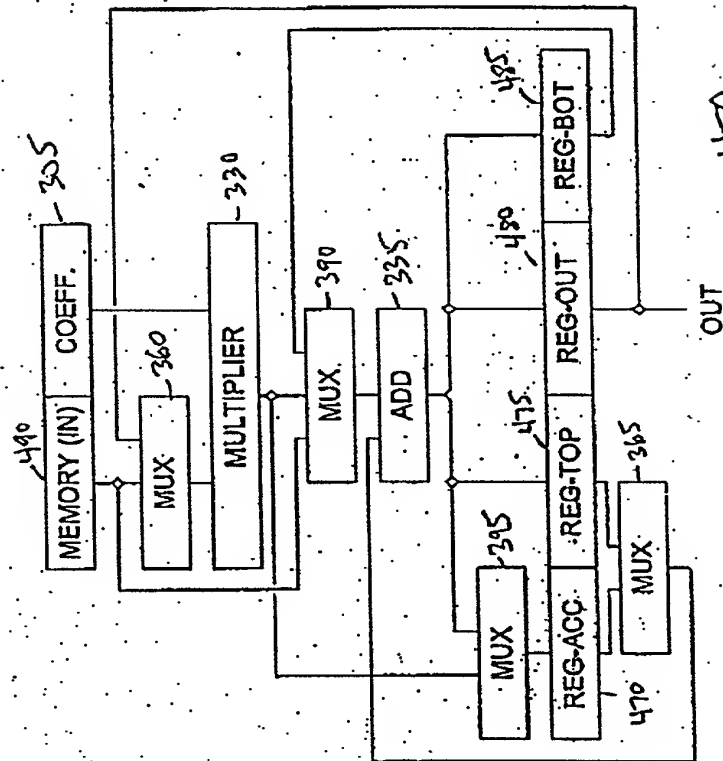
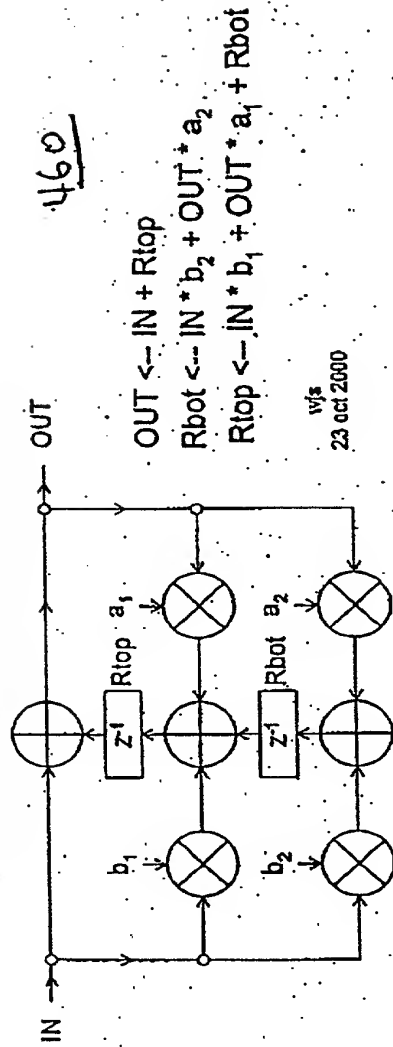
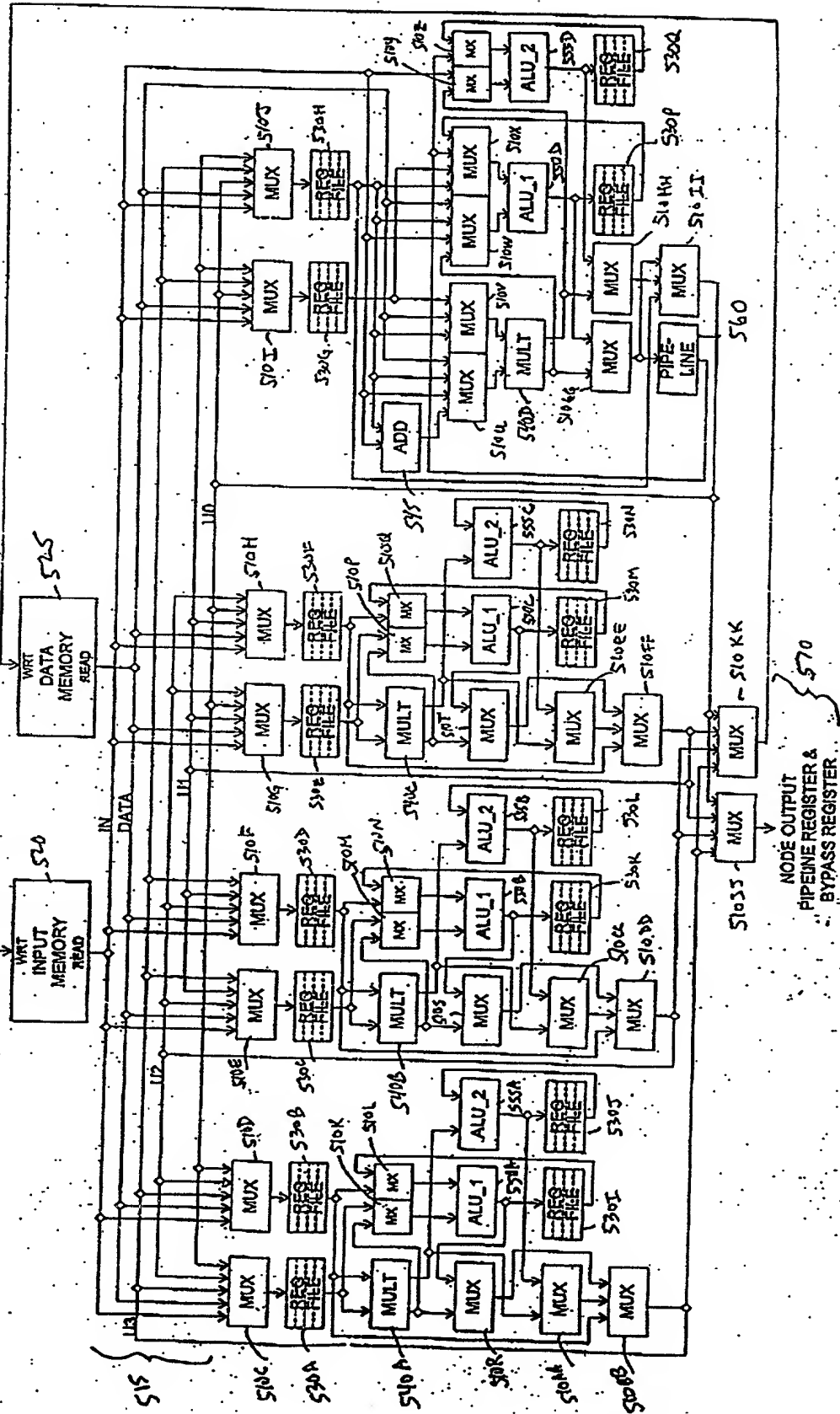
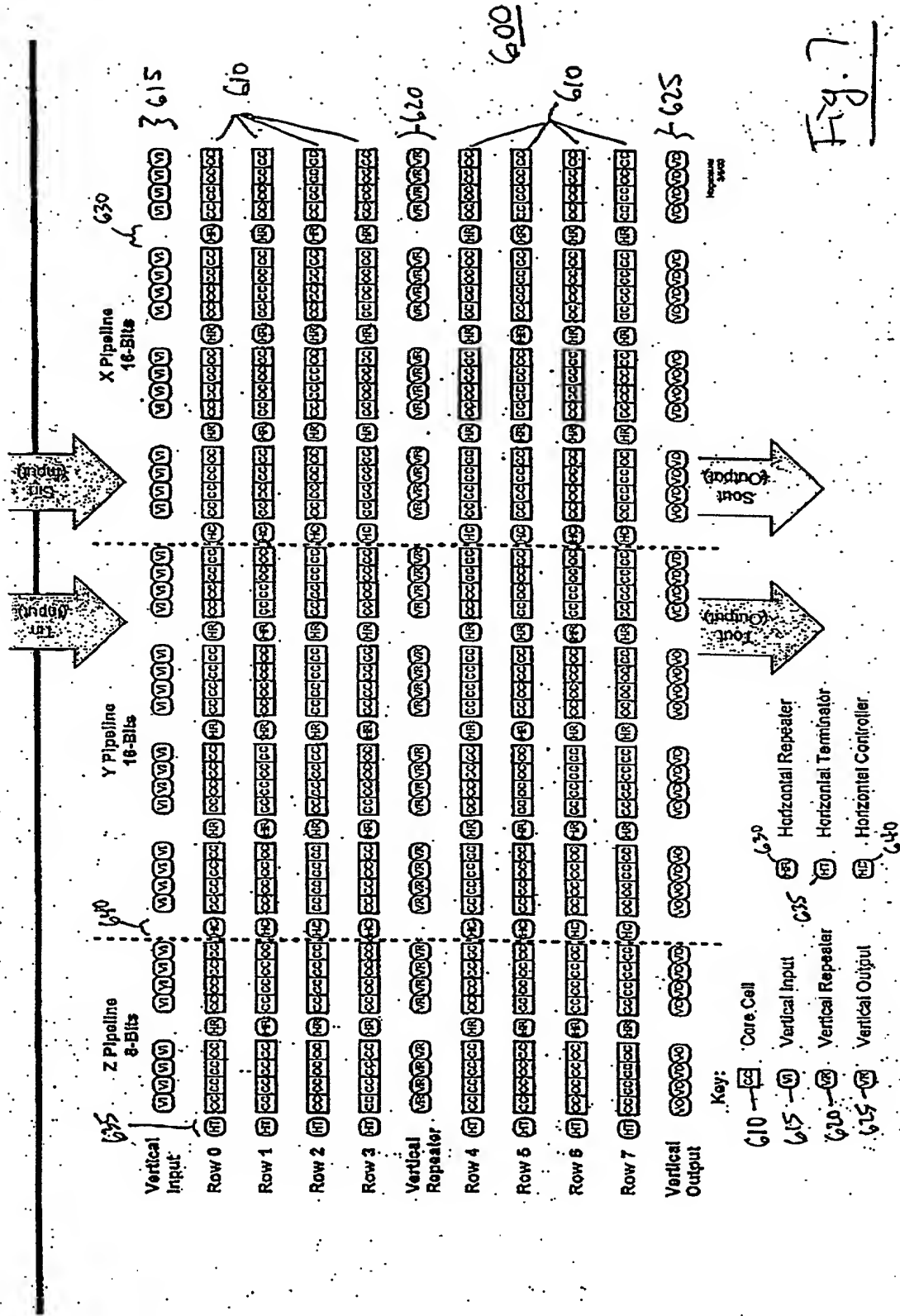


FIG. 5E





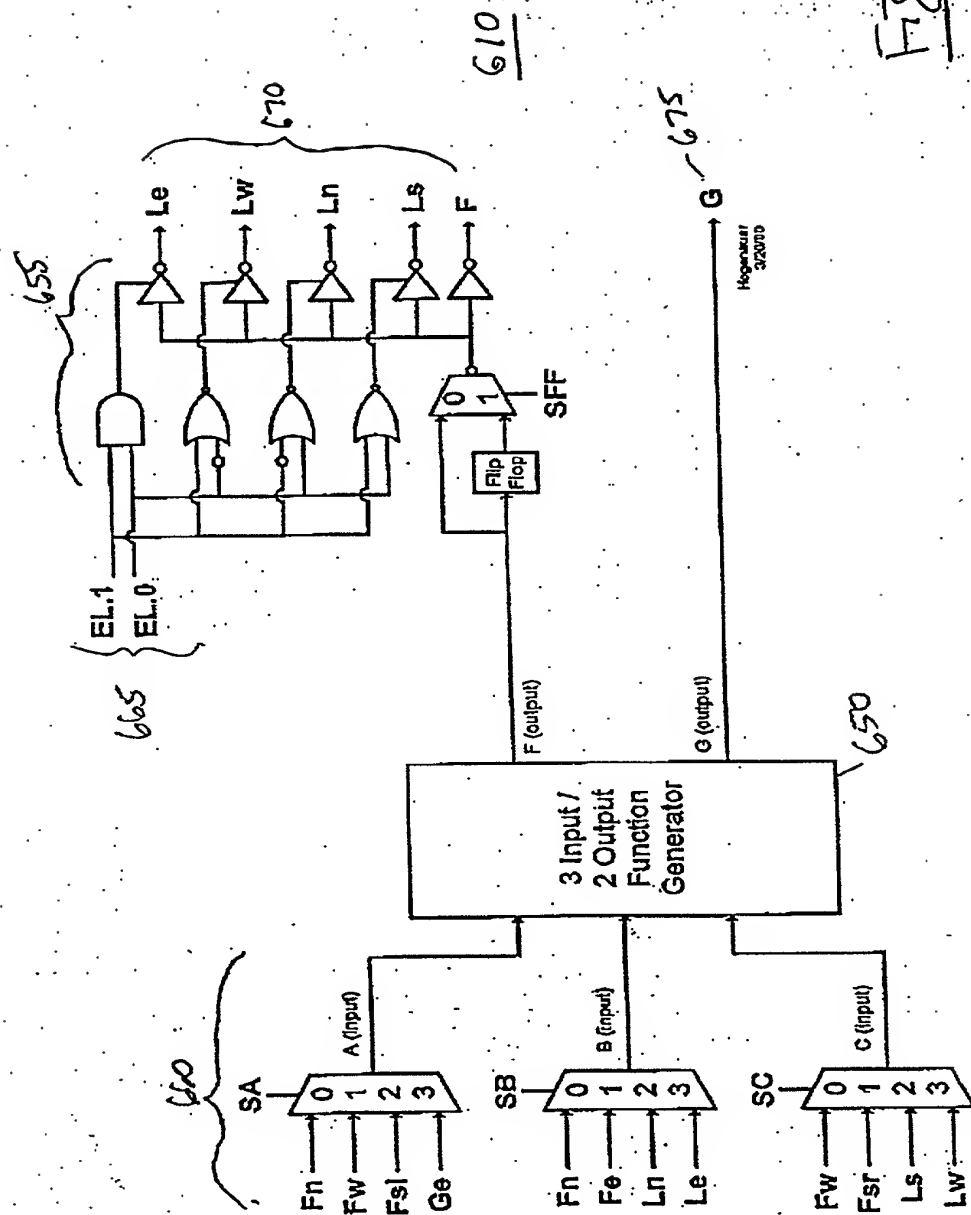


Fig. 8

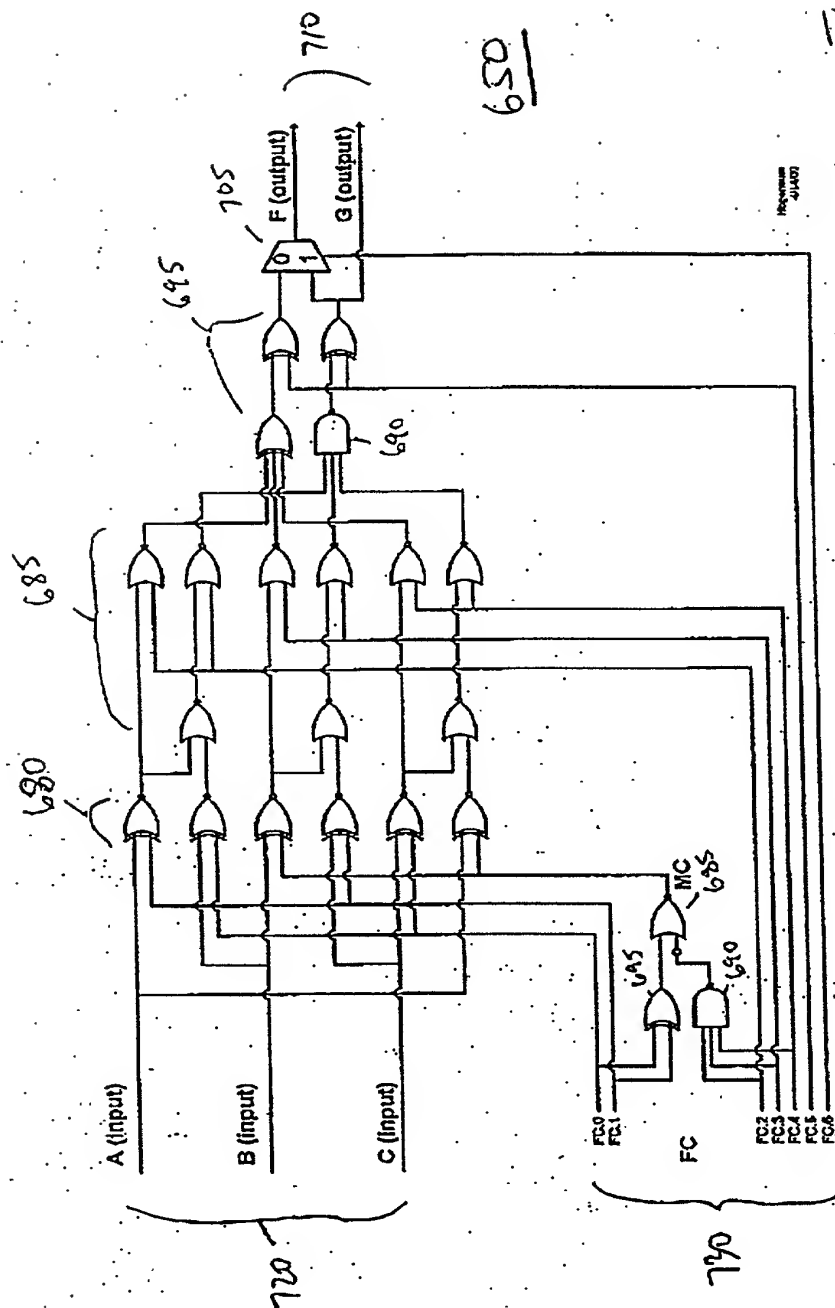
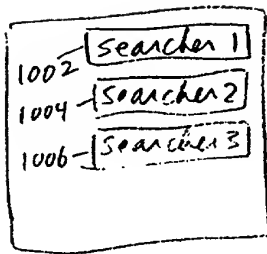
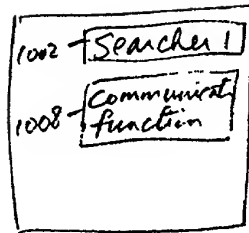


Fig. 9

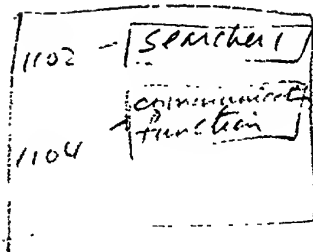


At power-up

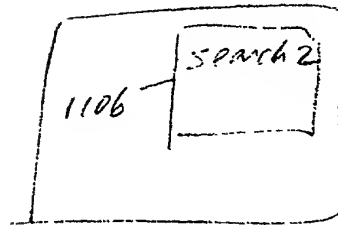


After system acquisition

FIG. 10

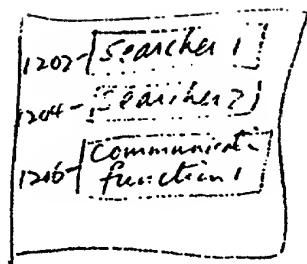


Before re-allocation

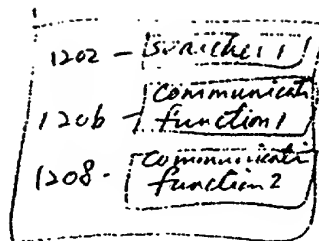


After re-allocation

FIG. 11

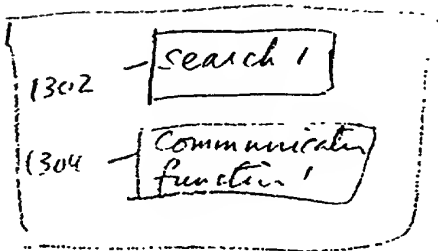


Before re-allocation

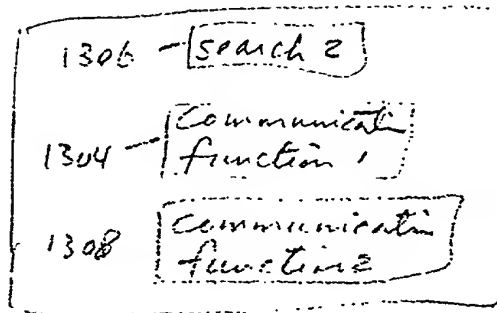


After re-allocation

FIG. 12



Before re-allocation



After re-allocation

FIG. 13